10/721966 TRANSMITTAL LETTER Docket No. (General - Patent Issued) END920030108US1 Budell et al. U.S. Patent No. **Issue Date** 6,978,214 B2 **December 20, 2005** Title: VALIDATION OF ELECTRICAL PERFORMANCE OF AN ELECTRONIC PACKAGE PRIOR TO **FABRICATION** Certificate **COMMISSIONER FOR PATENTS:** JAN 2 7 2006 Transmitted herewith is: Certificate of Correction - 2 pages of Correction Drawings - 5 pages **Postcard** No fee is required below. Errors were made by the U.S. Patent Office. No additional fee is required. A check in the amount of is attached. 09-0457 (IBM) as described below. Charge the amount of \boxtimes Credit any overpayment. Charge any additional fee required. ☐ Payment by credit card. Form PTO-2038 is attached. WARNING: Information on this form may become public. Credit card information should not be

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Just 1. Fredra

Dated: 01-23-06

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.: 6,978,214 B2

DATED : **December 20, 2005**

INVENTOR(S): Budell et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14

Line 49, delete "Delancy" and insert -- Delaney--

Column 15

Line 37, delete "N≥1" and insert --N≥--

Line 55, delete "step c" and insert --step e--

Line 56, delete "incrementing J" and insert --incrementing I--

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It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please delete, "Drawing Sheets 1, 2, 3, 4, and 9" because of the reprint quality.

Please insert -- Replacement Drawing Sheets 1, 2, 3, 4, and 9-. They are attached to this page.

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- SIGNAL
- GND
- ® VDD1
- O VDD2

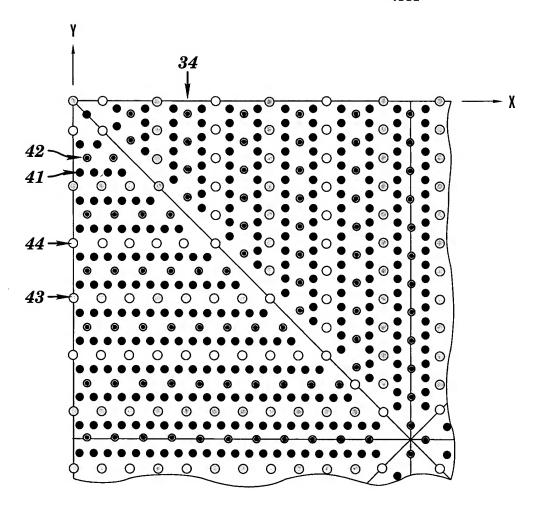


FIG. 2

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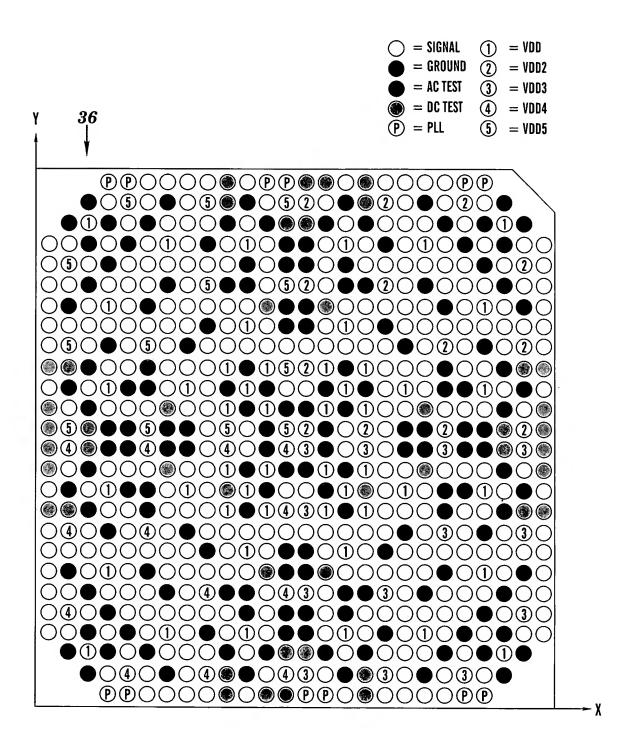
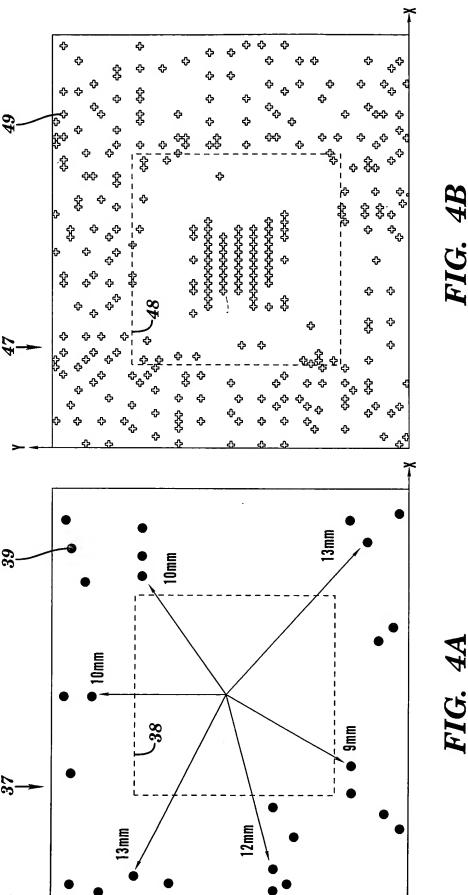


FIG. 3

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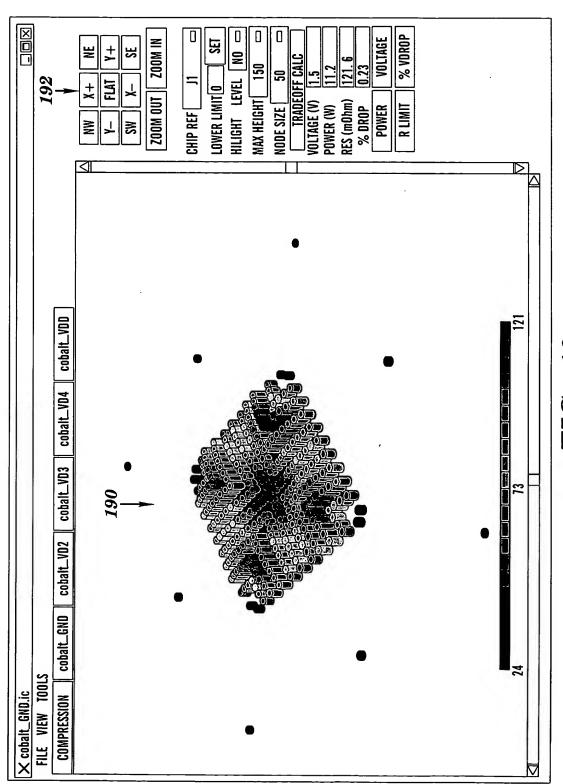


FIG. 10